

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PRIORITY Application Serial No.10/323,525
PRIORITY Filing DateDecember 18, 2002
Inventor Alan R. Reinberg
AssigneeMicron Technology, Inc.
PRIORITY Group Art Unit 2824
PRIORITY Examiner Christian D. Wilson
Attorney's Docket No. MI22-2491
Title: Microelectronic Device Fabricating Methods, and Methods of Forming
a Pair of Field Effect Transistor Gate Lines of Different Base Widths
From a Common Deposited Conductive Layer

PRELIMINARY AMENDMENT

To: Mail Stop Patent Application
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

VIA EXPRESS MAIL

From: Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 West First Avenue, Suite 1300
Spokane, WA 99201-3828

Please enter the following amendments prior to examining the above-
identified application.

AMENDMENTS